



Group art unit: 2811  
Examiner: Ori Nadav  
Conf. No.: 2358

Applicant(s): Hiroaki NAKANO

Serial No.: 10/756,366

Filing date: January 14, 2004

For: CIRCUIT BOARD FOR MOUNTING A  
SEMICONDUCTOR CHIP AND  
MANUFACTURING METHOD THEREOF

Atty. ref.: TAI 146

## AMENDMENT

March 27, 2007

**Mail Stop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Examiner Nadav:**

In response to the Examiner's Action mailed on January 3, 2007, please amend the above-identified application as follows:

OK to enter

O.N. 4/5/07